Contact:
Joe McCarthy
(408) 943-2902

Kimi Nishikawa
(408) 9434725

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CYPRESS DEBUTS RoboClock® II PROGRAMMABLE CLOCK BUFFER FOR NEXT-GENERATION COMMUNICATIONS APPLICATIONS

First Cypress 0.25-micron BiCMOS Product Delivers 185 MHz Performance

SAN JOSE, Calif., March 20, 2000 -- Cypress Semiconductor Corp. (NYSE:CY) today extended its leadership in the programmable clock chip market with the introduction of the RoboClock® II programmable clock buffer. RoboClock II devices, the first product manufactured on Cypress's new 0.25-micron BiCMOS process, deliver performance up to 185 MHz.

The RoboClock II devices build on the original RoboClock family's programmable skew, zero propagation delay, 50-50 duty cycle, and the ability to distribute a spread-spectrum signal. The RoboClock II family also expands on the original multiply and divide functions of 1,2, and 4, giving users the ability to multiply and divide by 1 through 6, 8, 10, and 12. The RoboClock II devices also add 10 more outputs to the original RoboClock devices' eight outputs, bringing the total to 18.

RoboClock II products also provide new features never before available in a programmable clock skew buffer. They offer user-selectable redundant reference clocks for fault tolerance. Each reference clock input can accommodate differential PECL, differential LVTTL, or single-ended LVTTL signals. The reference clock inputs are "hot swap" capable, allowing users to plug in a new board without powering down their systems.

"We have clearly taken the programmable clock market to a new level," said Mike Bollesen, strategic marketing manager for Communications Products. "Next-generation systems geared toward high-speed Internet communications are more complex and require greater performance than current clock skew products can deliver. The RoboClock II family gives designers the flexibility they need to build these systems."

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RoboClock devices allow designers to compensate for clock skews arising from varying circuit board trace lengths and to adjust device set-up and hold times. The RoboClock programmability allows designers to solve difficult board layout issues quickly, speeding time-to-market in networking, telecommunications, video graphics, and other applications.

RoboClock's "Spread Aware™"ability to distribute a spread-spectrum signal allows the EMI-reducing results of spread-spectrum timing technology to be realized throughout the system. Spread spectrum technology modulates the frequency of the system clock in a controlled way to spread the radio frequency interference (RFI) created by the clock and its system into several different radio frequency bands, making it easier for manufacturers to pass Federal Communications Commission RFI testing.

All key skew specifications of the RoboClock devices are tested and guaranteed, including pin-to-pin skew, propagation delay, and rise and fall time. Cypress tests and guarantees RoboClock's skew specifications, ensuring reliable operation in customer systems.

RoboClock II is the first product of Cypress's new 3.3-volt, double-layer-metal, 0.25-micron BiCMOS process. The process provides an optimal mix of speed, power, and cost relative to competitive processes and will enable Cypress to efficiently integrate mixed-signal, memory, and high-speed logic circuits in high-speed physical-layer devices and wireless communications products for high-frequency RF applications.

Price and Availability

The RoboClock II family consists of the CY7B994V, which runs up to185 MHz, and the CY7B993V, which runs up to 100 MHz. Both are sampling with production volumes expected in the second quarter. They are housed in 100-pin TQFP packages. In 10,000-unit quantities, the CY7B933V costs $20 each, and the CY7B994V is priced at $25.

About Cypress

Cypress Semiconductor provides high-performance integrated circuit solutions "By Engineers. For Engineers.™" for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static

RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

More than two-thirds of Cypress's sales come from fast-growing datacom/telecom markets and dynamic companies such as 3Com, Alcatel, Cisco, Ericsson, Lucent, Motorola, and Nortel Networks. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 3,600 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at http://www.cypress.com or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at http://www.cypress.com/investor/index.html.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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